



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

MARK ANDERS

Application No.: 10/035,574

Filed: October 18, 2001

For: **TRANSITION ENCODED DYNAMIC BUS
CIRCUIT**

Art Group: 2611

Examiner: Sudhanshu C. Pathak

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted concurrently with the Request for Continued Examination. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

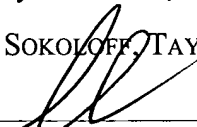
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Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: July 10, 2006



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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	10/035,574
				Filing Date	October 18, 2001
				First Named Inventor	Mark Anders
				Art Unit	2611
				Examiner Name	Sudhanshu C. Pathak
				Attorney Docket Number	42390P11426
Sheet	2	of	2		

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²	
		Anders, Mark, et al., "A Transition-Encoded Dynamic Bus Technique for High-Performance Interconnects", Symposium on VLSI Circuits, Digest of Technical Papers, pp. 16-17 (Jun. 13-15, 2002).		
		Anders, Mark, et al., "A Transition-Encoded Dynamic Bus Technique for High-Performance Interconnects", IEEE Journal of Solid-State Circuits, Vol. 38, No. 5, pp. 709-714 (May 2003).		
		Stan, Mircea R., et al., "Bus-Invert Coding for Low-Power I/O", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 3, No. 1, pp. 49-58 (Mar. 1995).		

Examiner Signature	Date Considered
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.
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